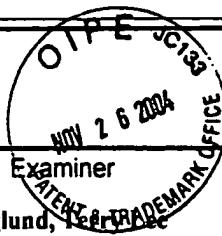


TRANSMITTAL OF FORMAL DRAWINGS

Docket No.
BUR920020054US1

In Re Application Of: Stephen F. Geissler



Application No.	Filing Date	Examiner	Customer No.	Group Art Unit	Confirmation No.
10/604,910	08/26/2003	Eglund	31647	2816	1909

Invention: LOW VOLTAGE CURRENT REFERENCE CIRCUITS

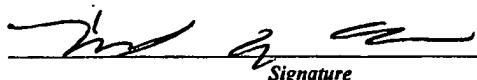
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12/14/04
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Transmitted herewith are:

2 sheets of formal drawing(s) for this application.

Each sheet of drawing indicates the identifying indicia suggested in 37 CFR Section 1.84(c).


Signature

Dated: 11-23-04

Richard A. Henkler, Reg. 39220
IP Law Department, Mail Stop 972E
IBM Corporation
1000 River Street
Essex Junction, VT 05452
Telephone: 802-769-8585

I hereby certify that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail in an envelope addressed to "Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450" [37 CFR 1.8(a)] on


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